

<b>Form PTO-1449</b> <b>INFORMATION DISCLOSURE CITATION</b> <b>IN AN APPLICATION</b> <i>(Use several sheets if necessary)</i>				Docket Number (Optional) <b>2269-5351.1US</b> <b>(02/0239.01/US)</b>		Application Number <b>Not Yet Assigned</b> <b>101800,058</b>	
				Applicant <b>Teck Kheng Lee</b>			
				Filing Date <b>March 12, 2004</b>		Group Art Unit <b>2814</b> <b>Unknown</b>	

  

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
TD	1	3,392,442	07/1968	Napier et al.		
	2	5,767,010	06/1998	Mis et al.		
	3	5,773,359	06/1998	Mitchell et al.		
	4	5,872,399	02/1999	Lee		
	5	6,107,180	08/2000	Munroe et al.		
	6	6,111,321	08/2000	Agarwala		
	7	6,201,305 B1	03/2001	Darveaux et al.		
	8	6,249,044 B1	06/2001	Kao et al.		
	9	6,316,828 B1	11/2001	Tao et al.		
	10	6,399,417 B1	06/2002	Lee et al.		
	11	6,404,064 B1	06/2002	Tsai et al.		

  

FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

  

OTHER DOCUMENTS		(Including Author, Title, Date, Pertinent Pages, Etc.)
TD		http://www.maxim-ic.com/1st_pages/UCSP.htm, "Wafer-Level Chip-Scale Package," Dallas Semiconductor, date unknown, 10 pages.
TD		"CSP Present and Future," date unknown, 3 pages.
TD		TARTER et al., "Ball Grid Array Performance Characteristics; A User's Design Guide," File Creation Date: March 31, 1999.

  

EXAMINER <b>2/Pran</b>	DATE CONSIDERED <b>10/26/04</b>
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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U.S. PATENT DOCUMENTS						
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FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO

  

OTHER DOCUMENTS			(Including Author, Title, Date, Pertinent Pages, Etc.)
TD		"BGA (Ball Grid Array)," National Semiconductor Corporation, September 1999, pp. 1-7, Application Note 1126.   —	
TD		MicroStar BGA Packaging Reference Guide, September 1999, 15 pages, Literature Number: SSYZ015B.   —	
TD		"Mounting Technology," Sharp, File Creation Date: October 10, 2000, pp. 12-36.   —	
TD		"Chapter 7: Board Design and Layout Considerations," FBGA User's Guide, April 12, 2002, pp. 41-45, Version 4.1.   ✓	

  

EXAMINER <div style="font-size: 1.5em; font-family: cursive;">Z. Brown</div>	DATE CONSIDERED <div style="font-size: 1.5em; font-family: cursive;">10/26/04</div>
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